

Quadruple \overline{S} - \overline{R} Latches

REJ03D0474-0400 Rev.4.00 May 10, 2006

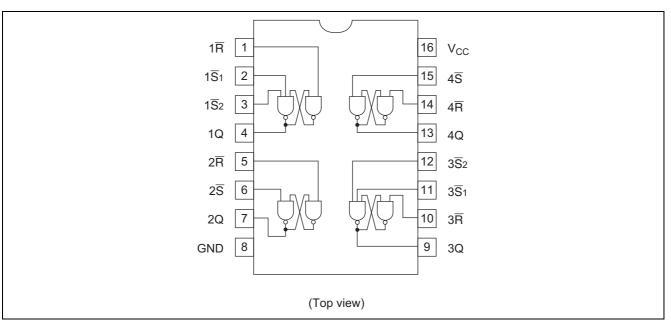
Features

• Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)	
HD74LS279P	DILP-16 pin	PRDP0016AE-B (DP-16FV)	Р	_	
HD74LS279FPEL	SOP-16 pin (JEITA)	PRSP0016DH-B (FP-16DAV)	FP	EL (2,000 pcs/reel)	

Note: Please consult the sales office for the above package availability.

Pin Arrangement





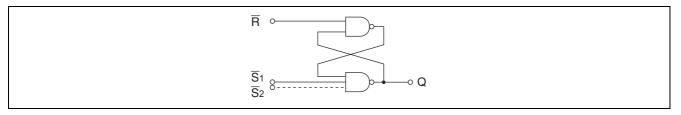
Function Table

Inp	Output			
<u>S</u> **	R	Q		
Н	Н	Q ₀		
L	Н	Н		
Н	L	L		
L	L	H*		

Notes: 1. H; high level, L; low level

- 2. Q_0 ; The level of Q before the indidicated input conditions were established.
- 3. *; This output level is psodo stable; that is it may not persist when \overline{S} and \overline{R} inputs return to their inactive (high) level.
- 4. **; For latches with double \overline{S} inputs; H; both \overline{S} inputs high, L; one or both \overline{S} inputs low.

Block Diagram (1/4)



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	
Supply voltage	V _{CC}	7	V	
Input voltage	V _{IN}	7	V	
Power dissipation	P _T	400	mW	
Storage temperature	Tstg	-65 to +150	°C	

Note: Voltage value, unless otherwise noted, are with respect to network ground terminal.

Recommended Operating Conditions

Item	Symbol	Min	Тур	Max	Unit
Supply voltage	V _{CC}	4.75	5.00	5.25	V
Output current	I _{OH}	—	—	-400	μΑ
Output current	I _{OL}	—	_	8	mA
Operating temperature	Topr	-20	25	75	°C



Electrical Characteristics

 $(Ta = -20 \text{ to } +75 \ ^{\circ}\text{C})$

Item	Symbol	min.	typ.*	max.	Unit	Condition		
Input voltage	V _{IH}	2.0			V			
input voltage	V _{IL}			0.8	V			
Output veltage	V _{OH}	2.7			V	$\label{eq:VCC} \begin{split} V_{CC} &= 4.75 \ \text{V}, \ V_{\text{IH}} = 2 \ \text{V}, \ V_{\text{IL}} = 0.8 \ \text{V}, \\ I_{OH} &= -400 \ \mu\text{A} \end{split}$		
Output voltage	V _{OL}			0.4	v	$I_{OL} = 4 \text{ mA} V_{CC} = 4.75 \text{ V}, V_{IH} = 2 \text{ V},$		
				0.5	v	I _{OL} = 8 mA V _{IL} = 0.8 V		
	I _{IH}			20	μΑ	$V_{CC} = 5.25 \text{ V}, \text{ V}_{I} = 2.7 \text{ V}$		
Input current	IIL			-0.4	mA	$V_{CC} = 5.25 \text{ V}, \text{ V}_{I} = 0.4 \text{ V}$		
	l _i			0.1	mA	$V_{CC} = 5.25 \text{ V}, \text{ V}_{I} = 7 \text{ V}$		
Short-circuit output current	los	-20		-100	mA	V _{CC} = 5.25 V		
Supply current**	Icc	_	3.8	7	mA	V _{CC} = 5.25 V		
Input clamp voltage	Vık		_	-1.5	V	$V_{CC} = 4.75 \text{ V}, I_{IN} = -18 \text{ mA}$		

Notes: * $V_{CC} = 5 V$, Ta = 25°C

** I_{CC} is measured with all \overline{R} inputs grounded, all \overline{S} inputs at 4.5 V, and all outputs open.

Switching Characteristics

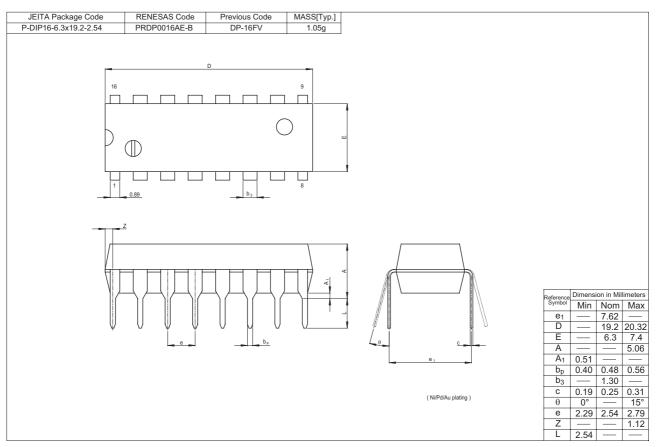
 $(V_{CC} = 5 V, Ta = 25^{\circ}C)$

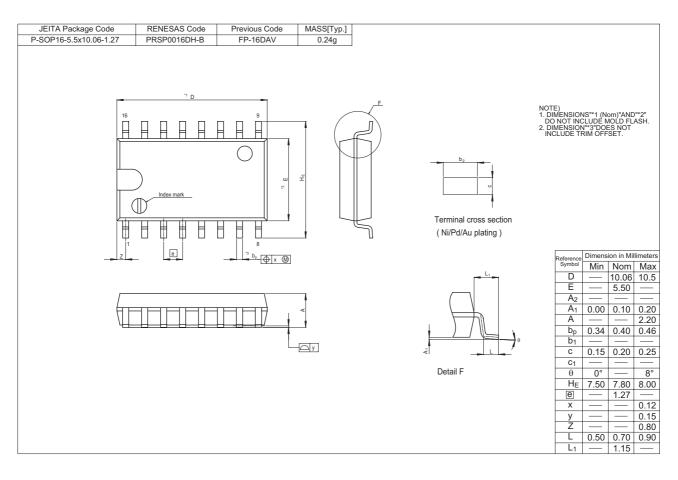
ltem	Symbol	Inputs	Output	min.	typ.	max.	Unit	Condition
Propagation delay time	t _{PLH}	S	Q	_	12	22	ns	C_L = 15 pF, R_L = 2 k Ω
	t _{PHL}			-	13	21		
	t _{PHL}	R		-	15	27		

Note: Refer to Test Circuit and Waveform of the Common Item "TTL Common Matter (Document No.: REJ27D0005-0100)".



Package Dimensions





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